4V Drive Nch+Nch MOSFET US6K2

Structure

Silicon N-channel MOSFET

Features

1) Two Nch MOSFETs are put in TUMT6 package.

- 2) High-speed switching, Low On-resistance.
- 3) 4V drive.

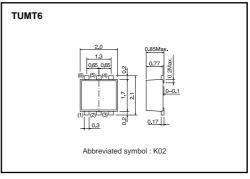
Applications

Switching

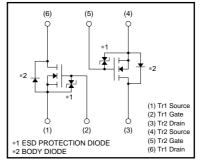
Packaging specifications

	Package	Taping
Туре	Code	TR
	Basic ordering unit (pieces)	3000
US6K2	·	0

•Dimensions (Unit : mm)



Inner circuit



•Absolute maximum ratings (Ta=25°C) <It is the same ratings for the Tr1 and Tr2>

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Parameter		Symbol	Limits	Unit
Drain-source voltage		VDSS	30	V
Gate-source voltage		Vgss	20	V
Ducia coment	Continuous	lь	±1.4	А
Drain current	Pulsed	I _{DP} *1	±5.6	А
Source current (Body diode)	Continuous	ls	0.6	А
	Pulsed	Isp *1	5.6	А
Total power dissipation		P₀ *2	1.0	W / TOTAL
		ΓD	0.7	W / ELEMENT
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C
14 Dux 40.00 Duty quele <10/				

*1 Pw≤10µs, Duty cycle≤1%*2 Mounted on a ceramic board

Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a)*	125	°C/W / TOTAL
	Rtn(cn-a)	179	°C/W / ELEMENT

* Mounted on a ceramic board



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Transistors

●Electrical characteristics (Ta=25°C)

< It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	lgss	-	-	10	μΑ	Vgs=20V, Vds=0V
Drain-source breakdown voltage	V(BR) DSS	30	-	_	V	I _D = 1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	-	-	1	μΑ	V _{DS} = 30V, V _{GS} =0V
Gate threshold voltage	VGS (th)	1.0	-	2.5	V	V _{DS} = 10V, I _D = 1mA
Static drain-source on-state resistance		-	170	240	mΩ	I _D = 1.4A, V _{GS} = 10V
	$R_{DS(on)^*}$	-	250	350	mΩ	I _D = 1.4A, V _{GS} = 4.5V
		-	270	380	mΩ	I _D = 1.4A, V _{GS} = 4V
Forward transfer admittance	Y _{fs} *	1	-	-	S	V _{DS} = 10V, I _D = 1.4A
Input capacitance	Ciss	-	70	_	pF	V _{DS} = 10V
Output capacitance	Coss	-	15	_	рF	V _{GS} =0V
Reverse transfer capacitance	Crss	-	12	_	рF	f=1MHz
Turn-on delay time	td (on) *	-	6	_	ns	Vdd≒ 15V
Rise time	tr *	-	6	_	ns	$I_{D} = 0.7A$
Turn-off delay time	td (off) *	-	13	-	ns	Vgs= 10V RL= 21Ω
Fall time	t _f *	-	8	-	ns	R _G =10Ω
Total gate charge	Qg *	-	1.4	2.0	nC	V _{DD} ≒15V, V _{GS} =5V
Gate-source charge	Q _{gs} *	-	0.6	-	nC	I _D = 1.4A
Gate-drain charge	Q _{gd} *	-	0.3	_	nC	R _L = 11Ω, R _G = 10Ω

*Pulsed

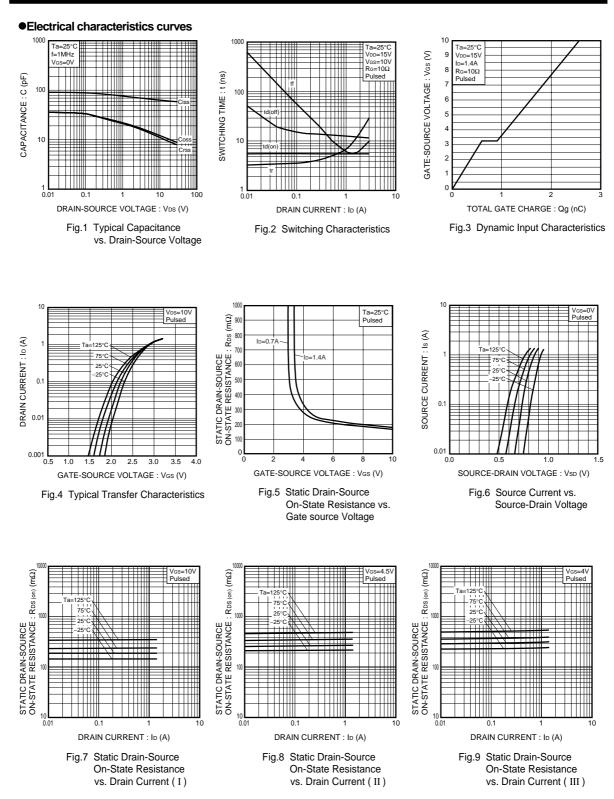
•Body diode characteristics (Source-drain) (Ta=25°C)

<It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsd	-	-	1.2	V	I _S = 0.6A, V _{GS} =0V

US6K2

Transistors



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